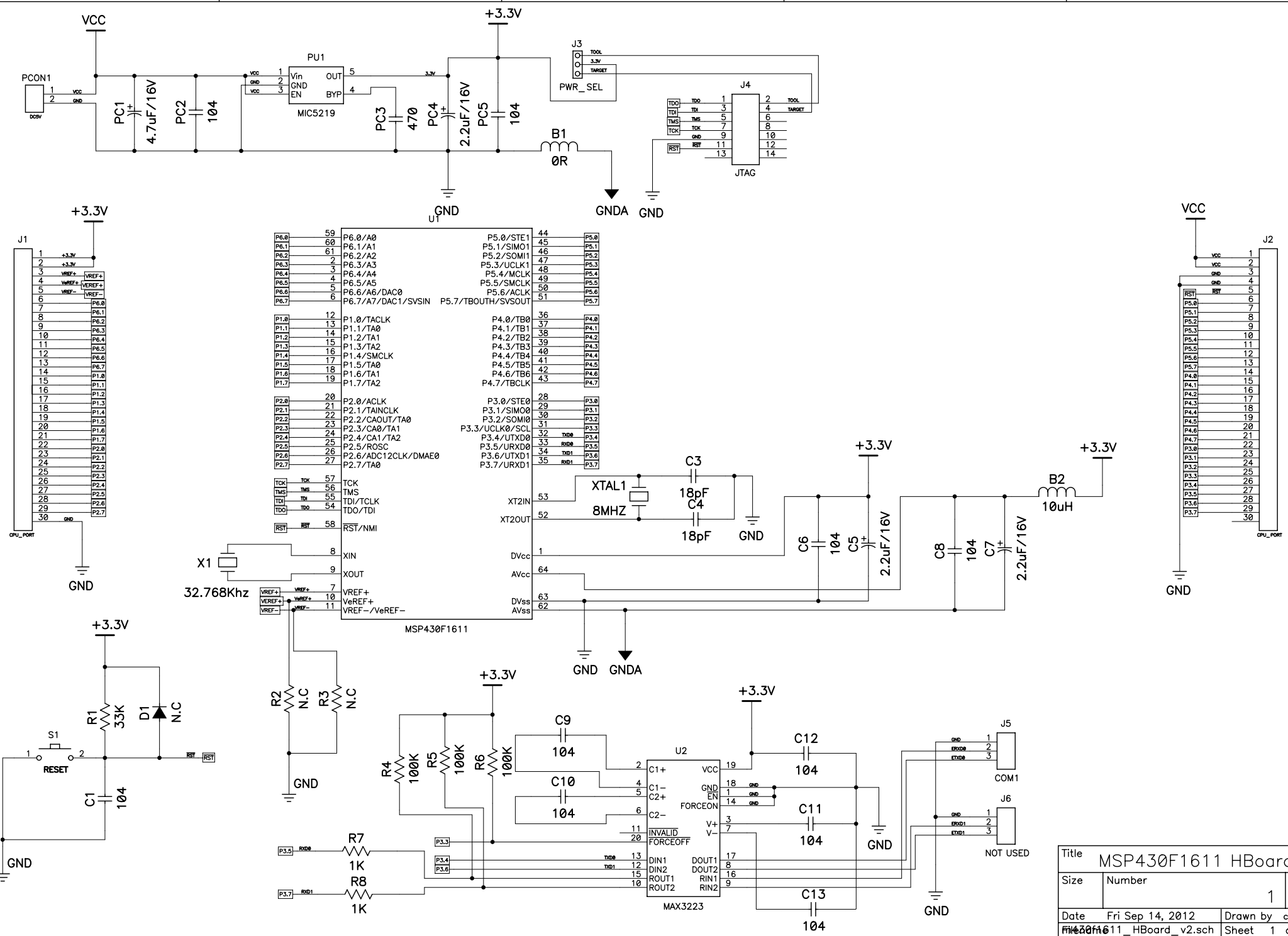
VCC
+3.3V
PCON1
DC5V
PC1
4.7uF/16V
PC2
104
PU1
Vin
GND
EN
OUT
BYP
MIC5219
PC3
470
PC4
2.2uF/16V
PC5
104
B1
0R
GNDA
GND
J3
TOOL
3.3V
TARGET
PWR_SEL
J4
TDO
TDI
TMS
TCK
RST
JTAG
J1
CPU_PORT
J2
U1
P6.0/A0
P6.1/A1
P6.2/A2
P6.3/A3
P6.4/A4
P6.5/A5
P6.6/A6/DAC0
P6.7/A7/DAC1/SVSIN
P5.0/STE1
P5.1/SIMO1
P5.2/SOMI1
P5.3/UCLK1
P5.4/MCLK
P5.5/SMCLK
P5.6/ACLK
P5.7/TBOUTH/SVSOUT
P1.0/TACLK
P1.1/TA0
P1.2/TA1
P1.3/TA2
P1.4/SMCLK
P1.5/TA0
P1.6/TA1
P1.7/TA2
P4.0/TB0
P4.1/TB1
P4.2/TB2
P4.3/TB3
P4.4/TB4
P4.5/TB5
P4.6/TB6
P4.7/TBCLK
P2.0/ACLK
P2.1/TAINCLK
P2.2/CAOUT/TA0
P2.3/CA0/TA1
P2.4/CA1/TA2
P2.5/ROSC
P2.6/ADC12CLK/DMAE0
P2.7/TA0
P3.0/STE0
P3.1/SIMO0
P3.2/SOMI0
P3.3/UCLK0/SCL
P3.4/UTXD0
P3.5/URXD0
P3.6/UTXD1
P3.7/URXD1
TCK
TMS
TDI/TCLK
TDO/TDI
RST/NMI
XIN
XOUT
VREF+
VeREF+
VREF−/VeREF−
XT2IN
XT2OUT
DVcc
AVcc
DVss
AVss
MSP430F1611
XTAL1
8MHZ
C3
18pF
C4
18pF
C6
104
C5
2.2uF/16V
C8
104
C7
2.2uF/16V
B2
10uH
X1
32.768Khz
R1
33K
D1
N.C
S1
RESET
C1
104
R2
N.C
R3
N.C
R4
100K
R5
100K
R6
100K
C9
104
C10
104
U2
C1+
C1−
C2+
C2−
VCC
GND
EN
FORCEON
V+
V−
INVALID
FORCEOFF
DIN1
DIN2
ROUT1
ROUT2
DOUT1
DOUT2
RIN1
RIN2
MAX3223
C12
104
C11
104
C13
104
J5
COM1
J6
NOT USED
R7
1K
R8
1K
Title
MSP430F1611 HBoard V2
Size
Number
1
Rev
1.0
Date
Fri Sep 14, 2012
Drawn by
cpuplaza
File
F1611_HBoard_v2.sch
Sheet
1
of
1